

## EAST Search History

| Ref # | Hits | Search Query  | DBs                             | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---------------------------------|------------------|---------|------------------|
| S1    | 57   | ((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4 same instruction near3 (buffer\$1 or register\$1) same (switch\$4 or replac\$4 or chang\$4 or backup)               | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/09 09:45 |
| S2    | 37   | "712".clas. ((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4 same instruction near3 (buffer\$1 or register\$1) same (switch\$4 or replac\$4 or chang\$4 or backup)   | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 13:47 |
| S3    | 32   | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) same instruction near3 (buffer\$1 or register\$1) with (switch\$4 or replac\$4 or chang\$4 or backup) | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 13:54 |
| S4    | 71   | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj3 (buffer\$1 or register\$1) with (switch\$4 or replac\$4 or chang\$4 or backup)       | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 13:55 |
| S5    | 29   | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj1 (buffer\$1 or register\$1) with (switch\$4 or replac\$4 or chang\$4 or backup)       | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 15:12 |
| S6    | 4    | "6895498"   | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 15:02 |
| S7    | 2    | "6205519".pn.   | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 15:04 |
| S8    | 2    | "6931639".pn.   | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 15:06 |
| S9    | 3    | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj1 (buffer\$1 or register\$1) with (exchang\$4 or swap\$4 or trad\$4)                   | US-PGPUB; USPAT; USOCR; DERWENT | AND              | OFF     | 2006/03/06 17:18 |

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|     |     |   |  |     |     |                  |
|-----|-----|---|--|-----|-----|------------------|
| S10 | 2   | "4831623".pn.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/06 15:47 |
| S11 | 15  | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj1 (buffer\$1 or register\$1) with (exchang\$4 or swap\$4 or trad\$4 or interchang\$4 or replac\$4)                         | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/06 17:26 |
| S12 | 17  | "712".clas. (((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj1 (buffer\$1 or register\$1) with (exchang\$4 or swap\$4 or trad\$4 or interchang\$4 or replac\$4 or "back up" or flip\$4) | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/06 17:28 |
| S13 | 18  | ((plurality or multitude or two or several ) near2 thread\$4) or multithread\$4) instruction adj1 (buffer\$1 or register\$1) with (exchang\$4 or swap\$4 or trad\$4 or interchang\$4 or replac\$4)                                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/06 17:28 |
| S14 | 246 | (althernat\$4 or switch\$4 or exchang\$4 or replac\$4 or interchang\$4) with instruction near3 (buffer\$1 or register\$1 or queue\$1) \$4thread\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/09 10:04 |
| S15 | 108 | "712".clas. (althernat\$4 or switch\$4 or exchang\$4 or replac\$4 or interchang\$4) with instruction near3 (buffer\$1 or register\$1 or queue\$1) \$4thread\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/09 10:18 |
| S16 | 300 | 712/214.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/09 10:51 |
| S17 | 37  | "712".clas. (althernat\$4 or exchang\$4 or replac\$4 or interchang\$4) with instruction near3 (buffer\$1 or register\$1 or queue\$1) \$4thread\$3   | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/09 10:09 |
| S18 | 28  | "712".clas. (althernat\$4 or exchang\$4 or replac\$4 or interchang\$4) with instruction near3 (buffer\$1 or register\$1 or queue\$1) (threads or multithread\$4)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>DERWENT | AND | OFF | 2006/03/09 10:09 |

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|     |     |   |                                 |     |     |                  |
|-----|-----|---|---------------------------------|-----|-----|------------------|
| S19 | 49  | "712".clas. (althernat\$4 or switch\$4 or exchang\$4 or replac\$4 or interchang\$4) same instruction near3 (buffer\$1 or register\$1 or queue\$1) same miss \$4thread\$3                  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:18 |
| S20 | 63  | "712".clas. (althernat\$4 or switch\$4 or exchang\$4 or replac\$4 or interchang\$4) same instruction near3 (buffer\$1 or register\$1 or queue\$1) same (miss\$4 or stall\$4) \$4thread\$3 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:02 |
| S21 | 32  | "712".clas. instruction near3 (register\$1 or buffer\$1 or queue\$1) near8 (correspond\$4 or associat\$4 or relat\$4 or attach\$4) near8 (thread or strand)                               | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:22 |
| S22 | 9   | 712/214.ccls. (miss\$4 or stall\$4) same (switch\$4 or chang\$4) near5 threads  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:53 |
| S23 | 5   | 712/214.ccls. (switch\$4 or chang\$4) near5 threads same instruction near3 (buffer\$1 or register\$1 or queue\$1)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:55 |
| S24 | 6   | 712/214.ccls. (switch\$4 or chang\$4) with (threads or context) same instruction near3 (buffer\$1 or register\$1 or queue\$1)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:56 |
| S25 | 23  | 712/214.ccls. (switch\$4 or chang\$4) with (thread\$1 or context)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:58 |
| S26 | 14  | 712/214.ccls. (switch\$4 or chang\$4) with (thread\$1 or context) same (buffer or register or queue)  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 10:58 |
| S27 | 77  | "712".clas. (alternat\$4 or switch\$4 or exchang\$4 or replac\$4 or interchang\$4) same instruction near3 (buffer\$1 or register\$1 or queue\$1) same (miss\$4 or stall\$4) \$4thread\$3  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:02 |
| S28 | 34  | "712".clas. (alternat\$4) same instruction near3 (buffer\$1 or register\$1 or queue\$1) same (miss\$4 or stall\$4) \$4thread\$3   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:15 |
| S29 | 769 | "712".clas. (switch\$4 or chang\$4) near3 (thread\$1 or context) same instruction\$1  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:15 |

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|     |     |   |                                 |     |     |                  |
|-----|-----|---|---------------------------------|-----|-----|------------------|
| S30 | 480 | "712".clas. (switch\$4 or chang\$4) near3 (thread\$1 or context) with instruction\$1  | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:39 |
| S31 | 11  | "712".clas. (switch\$4 or chang\$4) near3 (thread\$1 or context) with instruction\$1 with (exchang\$4 or interchang\$4 or swap\$4)                      | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 11:35 |
| S32 | 54  | "712".clas. (switch\$4 or chang\$4) near3 (thread\$1 or context) with (exchang\$4 or interchang\$4 or swap\$4 or backup)                                | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 12:34 |
| S33 | 28  | "712".clas. register near5 renaming with pool   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 16:30 |
| S34 | 0   | "712".clas. register near5 renaming with pool with (advantage or control or efficient or power)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 12:47 |
| S35 | 2   | "712".clas. register near5 renaming with pool same (advantage or control or efficient or power)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 12:48 |
| S36 | 138 | execut\$4 near5 two near5 threads with (simultaneously or "same time" or "at once")   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 16:39 |
| S37 | 188 | (execut\$4 or run\$4) near5 two near5 threads with (simultaneously or "same time" or "at once")   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 16:30 |
| S38 | 7   | execut\$4 near5 two near5 threads with (simultaneously or "same time" or "at once") with (advantage or parallel or efficien\$4 or speed or performance) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 16:49 |
| S39 | 5   | execut\$4 near5 two near5 threads with (simultaneously or "same time" or "at once") with (advantage or efficien\$4 or speed or performance)             | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/09 16:50 |
| S40 | 162 | (switch\$4 or chang\$4) near5 (thread\$1 or context) with (backup or secondary)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/10 10:10 |
| S41 | 25  | "712".clas. (switch\$4 or chang\$4) near5 (thread\$1 or context) with (backup or secondary)   | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2006/03/10 10:10 |